

**Section I (Amendments to Claims)**

Following is a listing of claims 1-24, as amended herein, with markings to show changes made:

**Claim 1 (Previously Presented):** A method of forming an interconnect structure comprising the steps of:

providing a lower metal wiring layer having first metal lines located within a lower low-k dielectric;

depositing an upper low-k dielectric atop said lower metal wiring layer;

etching at least one portion of said upper low-k dielectric to provide at least one via to said first metal lines;

forming rigid dielectric sidewall spacers in said at least one via of said upper low-k dielectric, said dielectric sidewall spacers are of a material selected from the group consisting of SiCH, SiCOH, SiC and SiO<sub>2</sub>; and

forming second metal lines in said at least one portion of said upper low-k dielectric.

**Claim 2 (Original):** The method of Claim 1 wherein said upper low-k dielectric and said lower low-k dielectric comprise materials having a dielectric constant ranging from about 1.0 to about 3.5.

**Claim 3 (Original):** The method of Claim 1 wherein said upper low-k dielectric and said lower low-k dielectric comprise low-k polymers or low-k carbon doped oxides.

**Claim 4 (Cancelled).**

Claim 5 (Currently amended): The method of Claim [[4]]1 wherein said forming rigid dielectric sidewall spacers further comprises:

depositing a conformal rigid dielectric liner atop said upper low-k dielectric and within said at least one via; and

etching horizontal surfaces of said conformal rigid dielectric liner to form said rigid dielectric spacers positioned on vertical sidewalls of said at least one via.

Claim 6 (Original): The method of Claim 5 wherein depositing a conformal rigid dielectric liner further comprises physical vapor deposition (PVD), plasma enhanced chemical vapor deposition (PECVD), high density plasma chemical vapor deposition (HDPCVD), or low pressure chemical vapor deposition (LPCVD).

Claim 7 (Original): The method of Claim 6 wherein said conformal rigid dielectric liner has a thickness ranging from about 10 nm to about 100 nm.

Claim 8 (Original): The method of Claim 7 wherein said etching horizontal surfaces of said conformal rigid dielectric liner further comprises an anisotropic etch process.

Claim 9 (Previously Presented): The method of Claim 8 wherein said lower metal wiring layer further comprises a rigid insulating layer deposited atop said lower low-k dielectric, said rigid insulating layer material selected from the group consisting of SiC, SiO<sub>2</sub>, and Si<sub>3</sub>N<sub>4</sub>.

Claims 10-24 (Cancelled).